



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : KAN et al.
Application No. : 10/760,309
Filed : January 21, 2004
Title : INTEGRATED CIRCUIT STRUCTURE AND A DESIGN
METHOD THEREOF
Group Art Unit : 2825
Examiner : Unassigned
Attorney Docket : BHT/3111-404

MAIL STOP - MISSING PARTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to examination, please amend the above-identified application as follows: